IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of Yoshikazu KASUYA Serial No.: 09/963,903 Filing Date: Sept. 26, 2001 METHODS FOR MANUFACTURING SEMICONDUCTOR DEVICES AND

SEMICONDUCTOR DEVICES

Group Art Unit: 2826

Examiner: Mandala, Victor A

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Assistant Commissioner for Patents Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated August 23, 2002, the response being due by September 23, 2002, please enter and consider the following.

Applicant hereby elects to prosecute Group II, claims 1-13, drawn to methods of manufacturing a semiconductor device.

It is believed that no fees are due. However, if a fee is necessary for consideration of this paper, please charge the fee to Deposit Account 50-0585. A copy of this paper is enclosed."

Respectfully submitted,

Alan S. Raynes

Dated: September 23, 2002

Reg. No. 39,809

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Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class, mail in an envelope addressed to: Assistant Commissioner, for Patents, Washington, D.C. 20231 on September 23, 2002.

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Alan S. Raynes